

V₃Si: AN ALTERNATIVE THIN FILM MATERIAL FOR SUPERCONDUCTING RF CAVITIES*

C. Benjamin^{1†}, R. Valizadeh¹, D. Seal¹, O. B. Malyshev¹, J. Conlon.

ASTeC, STFC Daresbury Laboratory, Sci-Tech Daresbury, Warrington, UK

G. B. G. Stenning. STFC RAL, Rutherford Appleton Laboratory, Didcot, UK

¹also at the Cockcroft Institute, Sci-Tech Daresbury, Warrington, UK

Abstract

Superconducting materials such as: V₃Si, NbN, NbTiN and Nb₃Sn, are potential alternatives to Nb for next generation thin film SRF cavities. In comparison to the Nb, their relatively high critical temperature (T_c) could allow for operation at higher temperatures (≥ 4 K) and the higher critical field could lead to higher accelerating gradients. The deposition of thin film V₃Si using single target high-power impulse magnetron sputtering is investigated on sapphire and niobium substrates. We report on the T_c and relate it to thin film characterisation using X-ray photoelectron spectroscopy, scanning electron microscopy and grazing incidence X-ray diffraction.

INTRODUCTION

Current generation bulk Nb superconducting RF (SRF) cavities are approaching their theoretical limit for performance and next generation accelerators are demanding higher accelerating gradients at higher operational temperatures. Alternative superconducting materials, such as, V₃Si and Nb₃Sn are promising candidates due to their relatively high critical temperatures of $T_c = 18$ and 17 K, and upper critical fields (H_c) of 720 and 540 mT respectively [1]. These properties may allow for production of thin film SRF cavities that can operate at higher temperatures (≥ 4 K) and higher accelerating gradients. This would reduce the complexity of the cryogenic systems and the infrastructure of the particle accelerator [2].

V₃Si is a cubic A15 phase superconductor. In bulk form, Hardy *et. al.* measured a critical temperature (T_c) of 17.1 K whilst also reporting the effect of impurities of less than 0.1% can drastically reduce T_c [3]. V₃Si thin films have been manufactured following a few different methods with varying levels of success: Zhang *et. al.* deposited a vanadium (V) thin film on a silicon (Si) layer and annealed the sample in high vacuum at temperatures ranging from 650°C to 900°C , forming V₃Si by interdiffusion of the V into the Si, resulting in a $T_c \geq 13$ K [4]. Deambrosis *et. al.* deposited using dc magnetron co-sputtering and reactive sputtering techniques introducing silane to the process gas to improve Si stoichiometry. On sapphire substrates they reported a T_c of 16.8 K, while on Nb substrates no superconducting phase was observed, due to the large diffusion rate between V and Nb [5].

* This work has been supported by: the IFAST collaboration which has received funding from the European Union's Horizon 2020 Research and Innovation programme under Grant Agreement No 101004730.

† Christopher.Benjamin@stfc.ac.uk

A series of V₃Si thin films were deposited on both Nb and sapphire substrates and at varying temperatures using high impulse power magnetron sputtering (HiPIMS). The films are characterised using surface characterisation techniques: Scanning electron microscopy (SEM), X-ray photoelectron spectroscopy (XPS) and grazing incidence X-ray diffraction (GI-XRD). Superconducting performance was characterised by four-point probe (FPP) and SQUID vibrating-sample magnetometer (SQUID-VSM).

EXPERIMENTAL DETAILS

Sample Preparation

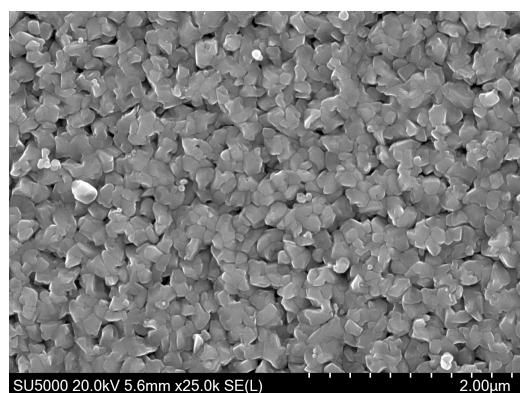
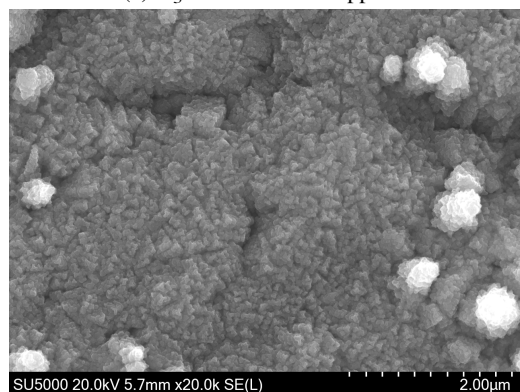
Nb foil and sapphire substrates were cleaned in isopropanol and acetone ultrasonic baths removing any adventitious carbon and other contaminants that may be present. The samples were loaded into an ultrahigh vacuum (UHV) load lock and heating to deposition temperature, $\geq 700^\circ\text{C}$, and left to thermally stabilise and for the sample stage to outgas over 12 hrs. Sample is then transferred into the deposition chamber at a pressure of $< 1 \times 10^{-8}$ mbar, whilst substrate is heated. Depositions were conducted using a HiPIMS power supply using the deposition parameters shown in Table 1. Kr was used as the process gas for all depositions at a partial pressure of 3×10^{-3} mbar.

Sample Characterisation

Structural characterisation was performed via GI-XRD using a Rigaku SmartLab with a 9 kW rotating anode Cu source. Thin film composition was obtained by XPS. Spectra were acquired using a non-monochromated Al $K\alpha$ x-ray source and a Thermo Alpha 110 hemispherical analyser. Survey and core region spectra were acquired with a pass energy of 50 and 20 eV respectively. Surface morphology was imaged by Hitachi SU500 SEM. T_c characterisation was conducted using a cryogenic four point probe for sapphire [6] and Quantum Design MPMS 3 SQUID-VSM.

Table 1: HiPIMS Deposition Parameters

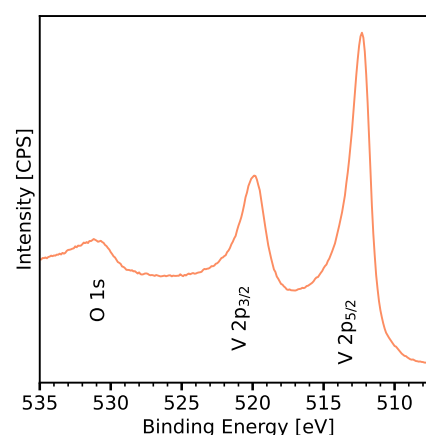
Deposition Parameters	Units	
Average Power	300	[W]
Frequency	1000	[Hz]
Pulse Length	10	[μs]
Duty Cycle	10	[%]
Deposition Length	3	[hr]

(a) V_3Si thin film on sapphire(b) V_3Si thin film on NbFigure 1: SEM images of V_3Si thin films on a) sapphire and b) Nb substrates.

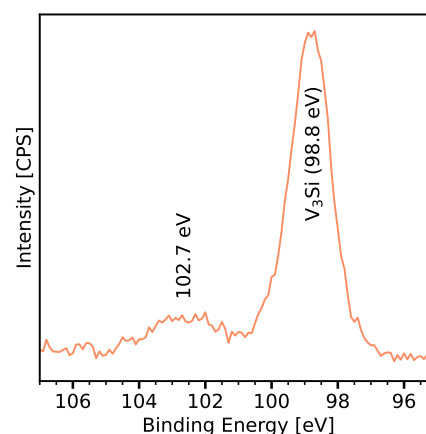
EXPERIMENTAL RESULTS AND DISCUSSION

Figure 1 displays the SEM images of the deposited V_3Si thin films on a) sapphire and b) Nb foil. On the sapphire substrate a V_3Si crystallite structure is observed with small voids existing between grains. This porosity suggests that there is a preferential nucleation on preexisting V_3Si sites instead of the sapphire. The overall topography is related solely to the growth of the film as the underlying sapphire wafer has a roughness of $R_a < 1$ nm. In contrast, the V_3Si crystallites are significantly smaller on the Nb foil which could be related to no preferential nucleation sites on the substrate. The larger structural features are due to the roughness of the Nb substrate used.

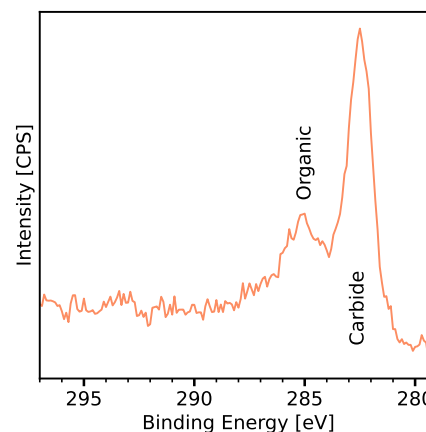
XPS was utilised to determine bulk elemental composition. Figure 2 shows the core region spectra: a) V 2p and O 1s b) Si 2p and c) C 1s present after 4 hours of Kr^+ ion beam sputtering. XPS spectra for the V 2p_{3/2} is at a binding energy (BE) of 512.3 eV which corresponds to reference data of metallic vanadium and the Si 2p is shifted by 0.7 eV to 98.7 eV from reference BE for silicon wafer. Contaminates are also present, two C 1s peaks are situated at 282.5 eV and 285.0 eV which aligns with carbide and C-C bonding. A broad O 1s peak is also present at 531.0 eV which couldn't be defined [7]. The oxygen and C-C contamination is likely from the sample being exposed to atmosphere before being



(a) V 2p



(b) Si 2p



(c) C 1s

Figure 2: XPS core region spectra of: V 2p, Si 2p, O 1s and C 1s of a V_3Si film on Nb substrate deposited using HiPIMS.

loaded into the XPS analysis system. Unfortunately, carbide is introduced during the deposition process and could be from the sputtering target, sample or system out gassing. Zhang *et al.* [4], measured BEs of 513.0 eV and 99.0 eV for V 2p_{3/2} and Si 2p respectively for multiple V-Si phases: V_5Si_3 , VSi_2 and V_3Si .

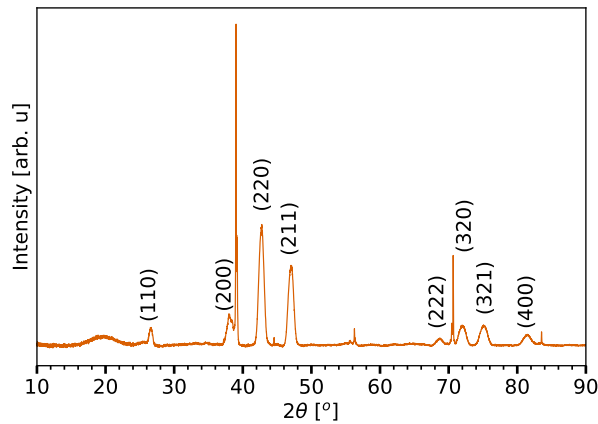


Figure 3: Grazing incidence X-ray diffraction of a V_3Si thin film on Nb (background subtracted). The labelled Miller indices correspond to V_3Si and the unlabelled peaks correspond to the Nb substrate.

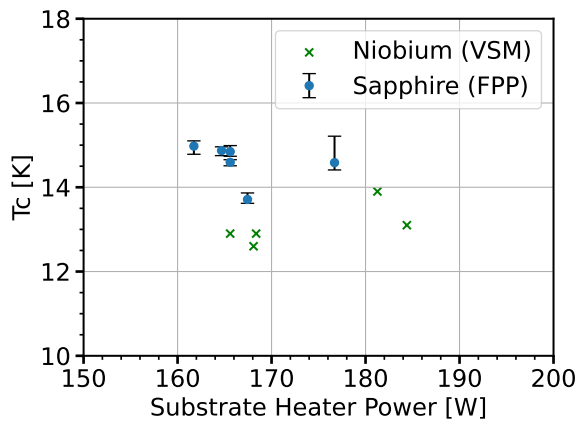


Figure 4: Measured T_c of a series of V_3Si thin films deposited on Nb and sapphire using HiPIMS, at range of substrate heater power.

Additionally, GI-XRD was conducted on the V_3Si film on Nb (Fig. 3) and confirms the formation of the superconducting A15 phase of V_3Si . The peaks associated with the V_3Si are broad suggesting small crystallites of random orientation, which is in agreement with the SEM images previously discussed. The XRD shows no presence of other V-Si phases and the unlabelled sharp peaks are associated with the Nb substrate.

Figure 4 shows the T_c of all the V_3Si depositions on both Nb and sapphire. T_c was obtained using a four point probe for sapphire and vibrating sample magnetometer for Nb substrates. Samples were deposited at varying substrate temperatures, monitored by the heater power. Due to the high temperature required for the formation of superconducting A15 V_3Si phase, the heater power was adjusted close to the systems maximum capability. The highest T_c measured was 14.85 K and 13 K for sapphire and Nb respectively. The lower T_c on the Nb substrates is likely related to the smaller

crystallite structure. The oxygen and carbon contamination present in the film explains the T_c being lower than the theoretical maximum on both substrates.

CONCLUSION

Experiments conducted show promise for V_3Si thin films deposited using HiPIMS. Superconducting V_3Si thin films have been successfully grown on sapphire inline with previous research and on Nb substrates. SEM shows a small crystallite structure which is in agreement in GI-XRD. The T_c s measured are still below bulk value which could be as a result of the crystal growth or the contamination observed in the XPS. Additionally, the variation between sapphire and Nb substrates is related to the formation of preferential nucleation sites.

FUTURE WORK

The deposition parameters can be explored further by: adjusting of the duty cycle, introducing Bipolar HiPIMS and increasing substrate temperature. This may show improvement of the V_3Si film by increasing crystallite size. UHV annealing techniques and post processing such as flash [8] or laser annealing is also desirable as it has the potential to improve film crystallisation and reduce defect states.

REFERENCES

- [1] A.-M. Valente-Feliciano, "Superconducting rf materials other than bulk niobium: A review," *Superconductor Science and Technology*, vol. 29, no. 11, p. 113 002, 2016. doi:10.1088/0953-2048/29/11/113002
- [2] G. Stewart, "Superconductivity in the a15 structure," *Physica C: Superconductivity and its Applications*, vol. 514, pp. 28–35, 2015, Superconducting Materials: Conventional, Unconventional and Undetermined. doi:10.1016/j.physc.2015.02.013
- [3] G. F. Hardy and J. K. Hulm, "Superconducting silicides and germanides," *Phys. Rev.*, vol. 89, pp. 884–884, 4 1953. doi:10.1103/PhysRev.89.884
- [4] W. Zhang *et al.*, "Thin-film synthesis of superconductor-on-insulator a15 vanadium silicide," *Scientific Reports*, vol. 11, no. 1, p. 2358, 2021. doi:10.1038/s41598-021-82046-1
- [5] S. M. Deambrosis *et al.*, "The Progress on Nb3Sn and V3Si," in *Proc. SRF'07*, Beijing, China, Oct. 2007. <https://jacow.org/srf2007/papers/WE203.pdf>
- [6] D. Seal *et al.*, "Characterisation facilities for evaluating superconducting thin films for SRF cavities," in *Proc. IPAC'23*, Venice, Italy, 2023, pp. 2983–2986. doi:10.18429/JACoW-IPAC2023-WEPA141
- [7] A. V. Naumkin, A. Kraut-Vass, S. W. Gaarenstroom, and C. J. Powell, *NIST x-ray photoelectron spectroscopy database*. doi:10.18434/T4T88K
- [8] L. Rebohle, S. Prucnal, Y. Berencén, V. Begeza, and S. Zhou, "A snapshot review on flash lamp annealing of semiconductor materials," *MRS Advances*, vol. 7, no. 36, pp. 1301–1309, 2022. doi:10.1557/s43580-022-00425-w